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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: DIMITRI LEDERER

SERIAL NO.: 10/572,799

FILED: March 22, 2006

FOR: METHOD OF MANUFACTURING A
MULTILAYER SEMICONDUCTOR
STRUCTURE WITH REDUCED OHMIC
LOSSES

CONFIRMATION: 4919

GROUP ART UNIT: 4193

EXAMINER: Slutsker, Julia

ATTY. REFERENCE: LEDE3001/JJC/JDG

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

The below identified communication(s) or document(s) is(are) submitted regarding the above patent application:

☒ Response

☐ Check in the Amount of \$

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☒ Please debit or credit **Deposit Account Number 02-0200** for any deficiency or surplus in connection with this communication.

☐ Small Entity Status is claimed.

☐ MESSAGE:

Customer 23364

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Date: August 1, 2008

Respectfully submitted,

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PATENT

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In re Application of:

Application No.:	10/572,799	Confirm. No.:	4919
Filing Date:	March 22, 2006	Examiner:	Slutsker, Julia
First Inventor:	Dimitri LEDERER	Art Unit:	4193
Attorney No.:	LEDE3001/JJC/JDG	Customer No.:	23364
For:	METHOD OF MANUFACTURING A MULTILAYER SEMICONDUCTOR STRUCTURE WITH REDUCED OHMIC LOSSES		

REQUEST FOR RECONSIDERATION IN
REPLY TO OFFICE ACTION
OF MAY 1, 2008

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INTRODUCTORY COMMENTS

This is responsive to the outstanding Office Action in the above-identified application. In view of the following remarks, reconsideration of the application is respectfully requested.